Curriculum- vitae

ASHISH KUMAR GUPTA

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CAREER OBJECTIVE

Looking for an opportunity to apply my skill and contribute to growth of the organization while helping create some advancement opportunity for myself.

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Sr. No.	Name of Organisation	Designation	Working Period	Experience		
1.	Govt. Polytechnic, Banda (U.P)	Guest Lecturer	July 2019 to till date	5.0 Years		
2.	Shatabdi Institute of Engineering & Research, Nashik (M.H)	Assistant Professor	Dec 2013 to June 2019	5.6 Years		
3.	HBTI. Kanpur (U.P).	Guest Lecturer	July 2012 to Nov 2013	1.5 Years		
4.	Prayag Institute of Technology& Management, Prayagraj (U.P)	Assistant Professor	July 2011 to June 2012	1.0 Years		
5.	Govt. Polytechnic, Banda(U.P)	Guest Lecturer	July2008 to May 2009	0.11 Years		

ACADEMIC QUALIFICATION

Sr. No.	Degree	Institute/University	Specialization	Year of Passing	Percentage	Remark
1	M.E	SGSITS, Indore /RGPV Bhopal (M.P)	Electrical Engineering (Power Electronics)	December 2011	65.27	1 st Class
2	B.tech	KCNIT Banda / UPTU Lucknow (U.P)	Electrical and Electronics Engineering	August 2007	61.56	1 st Class
4	12th	UP Board		June 2002	50.4	2 nd Class
5	10 th	UP Board		July 1999	55.55	2 nd Class

EDUCATIONAL EXPERIENCE

- Full time approval post of Assistant Professor in Electrical Engineering by Savitribai Phule Pune University, Pune (M.H), since 14/09/2014.
- Attend and participate TEQIP-III Sponsored Faculty Development Program conducted by "R.G.P.V", Bhopal (M.P) On 'Application of Green Energy Technologies for Environmental Sustainability' from 3rd April, 2018 to 7th April, 2018.
- Attend and participate AICTE Sponsored Staff Development Program conducted by "ITM, Gwalior" On 'MATLAB & SIMULINK based Technical programming in Teaching & Research' from 13th June, 2011 to 25th June, 2011.
- Attend and participate CE&QIP workshop conducted by "IIT Bombay" on "Introduction to Robotics" from 19-Feb 2011 to 20-Feb 2011.

- Attend and participate National workshop conducted by "Advanced Micronic Devices, Ltd, Mumbai" on "Cadence Or-CAD P-Spice, ALDEC Active HDL and Integrated Engineering Software's". In Collaboration with "IES-IPS Academy, Indore" held on 11-March 2010 to 12-March 2010.
- One month Industrial training from "National Thermal Power Corporation (NTPC)", Dadri (U.P)

RESERCH PAPER PUBLISHED

- Research paper published "Software and Hardware Implementation of DC-DC Buck Converter using Feasible Automatic Control", in Advanced Research in Electrical and Electronic Engineering (AREEE) Print ISSN: 2349-5804; Online ISSN: 2349-5812 Volume 2, Number 1 October-December (2014), International journal at Krishi Sanskriti Publications New Delhi, India", held on 10-December 2014 to12 - December 2014.
- Research paper published "A Novel Technique To Simulate Two Quadrant Dc Chopper Fed Drives", in Recent Trends in Simulation & Modeling Techniques in Electrical Engineering at "CGTTM-2011 Conference" at Lakshmi Narain College of Technology, held on 18-August 2011 to 20- August 2011.
- Research paper published "A Technique To Simulate Two Quadrant Dc Chopper Fed Drives", in NSCTEA 2011, National Conference & Seminar on Computational Techniques for Engineering Applications, "Corporate Institute Of Science And Technology, Bhopal", held on 4-May 2011 to 6-May 2011.
- Research paper published "Speed Control of Separately Excited Dc Motor by Modern Trend", in ECOMM-11, National Conference on Recent Trends in Electronics & Communication Technology, "SD Bansal College of technology & KCB technical academy, Indore", held on 8-April 2011 to 9-April 2011.

EXTRA CURICULLAR ACTIVITIES

- ➢ Work as a Senior Supervisor in Savitribai Phule, Pune University (M.H) Examination.
- Work as a NAAC Co-ordinator.
- Work as a Project Co-ordinator.

STRENGTH

Persistence and Self motivated

PERSONAL DETAILS

1.	Date of Birth	28-May1985
2.	Father's Name	Shri Ramsaran Vishnoi
3	Mother's Name	Smt. Manju Gupta
4.	Marital Status	Married
5.	AADHAR NO	257169349991
6.	PAN No.	AXWPG0735B

Declaration

I hereby declare that the above cited information is true to the best of my knowledge and belief, if given a chance, I can prove myself.

Place

Date